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PATENT  
5201-27500/03-1236IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re Application of:  
Hung et al.

Serial No. 10/692,091

Filed: October 23, 2003

For: MEMORY MODULE HAVING  
MIRRORED PLACEMENT OF DRAM  
INTEGRATED CIRCUITS UPON A  
FOUR LAYER PRINTED CIRCUIT  
BOARDGroup Art Unit: 2824  
Examiner: Dang T. Nguyen

Atty. Dkt. No. 03-1236 (5201-27500)

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Service as First Class Mail in an envelope addressed to:  
Commissioner for Patents, P.O. Box 1450, Alexandria, VA  
22313, on the date indicated below:

09/14/2005  
Date

Kevin L. Daffer

AMENDMENT: RESPONSE TO OFFICE ACTION MAILED JUNE 14, 2005Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Dear Sir/Madam:

Responsive to the Office Action of June 14, 2005, please amend the case as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this  
paper.

Remarks begin on page 8 of this paper.